

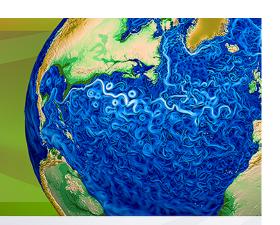
Guardians of the ACME and Only YOU Can Prevent Performance Fires

Phil Jones
On behalf of the Performance Team

ACME All-Hands
June 2017







Guardians of the ACME





I am groot

- I am groot
 - I am groot
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Performance Group Roles

- Hero role: v1 support
 - POCs, SEs, Coupled Group
 - Fixing bugs
 - PE Layouts, PIO config
- Performance optimization
 - Evaluating and enhancing performance
 - Focus on current sims, current machines
- Preparing for Transition
 - Preventing future disasters
 - Early access
 - Identifying changes needed
 - Prototyping new ideas



- Performance Team
 - Az, Noel, Jayesh, Pat, Phil, Ben, Min
 - ECP/CAAR: Matt N., Sarat, Erich





Hero Contributions

- Machine Specific (POCs)
 - Changes in environment
 - Modules, compilers, etc.
 - Batch systems
 - Sporadic slowdowns Edison
- PE Layouts (next slide)
- Bugs
 - I/O Stack: pio, pnetcdf, netcdf
 - CIME
 - Thread irreproducibility
 - Misc.
- Infrastructure review
- Improved testing coverage
 - Integrators, developers, machines









Biggest weapons/knob

- PE layouts
- See: Benchmark Results and Optimal Layout in Perf Confluence
- A_WCYCL2000 ne120_oRRS15 Titan 7448 —

SYPD:

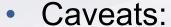
1.41

Charged CPUHours/SY

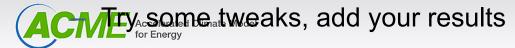
3.9M

Nodes:

7448



- Out-of-box results, YMMV
- Little/no I/O, YMMV
- Sometimes outdated
- Default PE layouts
- If you see something, say something









Internal Changes

- Nested threading in atm
- Threading in sea-ice
- CLUBB optimizations
- Atmospheric physics load balancing
- Improvements to initialization
 - Communication algorithms





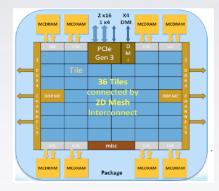




Preparing for Next Generation

- Nested threading, improved threading, affinity
- GPU acceleration
 - superparameterization
- Vectorization
- Integrated projects
 - ECP
 - CAAR, NESAP
 - CMDV
 - SciDAC
- Significant code refactoring
- All above likely to be 2x-3x range.
- Need algorithm changes, new ideas





KNL: 68 cores, 272 threads, Vector units, highbandwidth memory

GPU: SIMD cores, separate memory

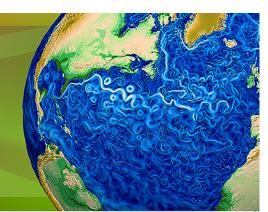


ARM????













Extreme

- Preventing high-priority sims
- Type 1 Incident Response, multiagency, aircraft, etc.

Very High

- Blocking of other sims (esp. high resolution), Substantial reduction in high-priority sim
- Type 2 Incident Response
- High
 - Significant performance hit
 - Hot Shot team
- Moderate
 - Known issue
 - Workaround (containment)
- Low
 - No immediate threat, let burn

Triage







At a minimum

Look at timing info:

Init Time	:	36.677 sed	conds		
Run Time	:	1117.133 sed	conds	37.238 sec	onds/day
<i>"</i>					
TOT Run 6.36 myears/		1117.133	seconds	37.238	seconds/mday
LND Run 222.96 myear	Time:		seconds	1.062	seconds/mday
ROF Run 6175.10 myea			seconds	0.038	seconds/mday
ICE Run 106.09 myear			seconds	2.231	seconds/mday
ATM Run 7.51 myears/	Time:		seconds	31.510	seconds/mday
OCN Run 41.77 myears		170.009	seconds	5.667	seconds/mday
GLC Run 0.00 myears/		0.000	seconds	0.000	seconds/mday
WAV Run 0.00 myears/		0.000	seconds	0.000	seconds/mday
CPL Run 14.72 myears		482.530	seconds	16.084	seconds/mday
CPL COMM 6.65 myears/		1068.084	seconds	35.603	seconds/mday



Remember-Only you can PREVENT THE MADNESS!





At a minimum

- Adjust PE layout
- Rules of thumb see Confluence:
 - How to Create PE Layout in Atm How-to
- But...always exceptions, Edisonian

ATM 675 tasks, 2 threads per task, 945 seconds, 7.5 SYPD OCN

128 tasks, 2 threads per task, 170.0 seconds, 42 SYPD

ICE: 512 tasks, 2 threads per task, 67 seconds, 107 SYPD

CPL 512 tasks, 2 threads per task 483 seconds LND 168 tasks, 2 threads per task, 32 seconds, 223 SYPD

1349



This shameful waste

WEAKENS AMERICA!

processor cores

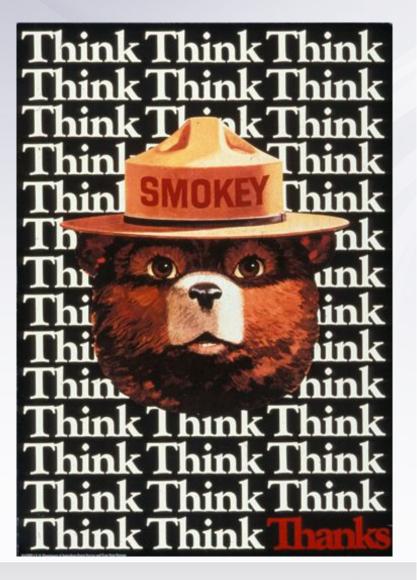
1024

1360

1615

U.S. DEPARTMENT OF ENERGY

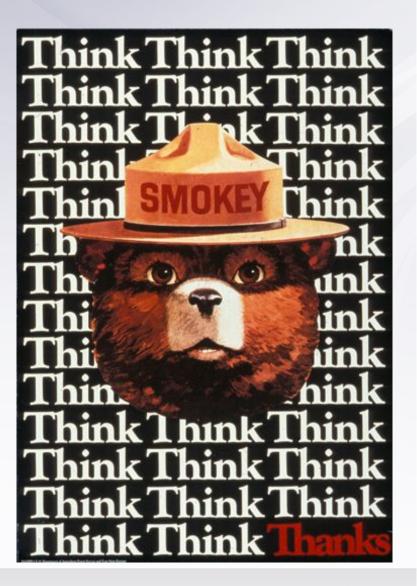
- Climate Lags Others
 - Failing readiness
 - Can't use GPU or KNL, but at least we scale poorly







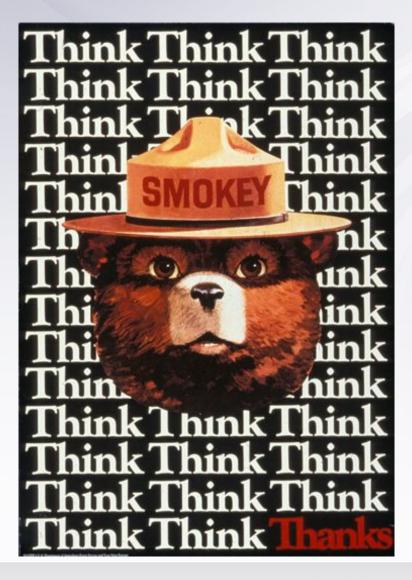
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 - Where performance comes from
 - Pros and Cons of abstractions
- Everyone must think
 - Performance is MY responsibility







